

# SAC3115



GaAs MMIC Power Amplifier  
13.5GHz~14.75GHz 40dBm

Rev2.1

## Features

- Frequency: 13.5GHz~14.75GHz
- Gain: 24dB
- Output P<sub>-1dB</sub>: 40dBm
- Supply Voltage: +8V
- Power-Added Efficiency: 38%
- Die Size: 3.51mm×3.51mm×0.1mm
- Packaged: Bare Die

## Typical Applications

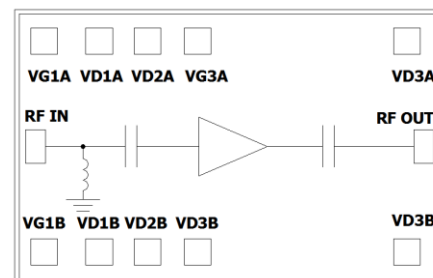
- Point-to-Point Radios
- SATCOM
- Military and Space
- Test and Measurement
- Radar

## General Description

SAC3115 is a Ku-band GaAs MMIC power amplifier. SAC3115 provides 24 dB of gain, and 40dBm of output power for 1 dB compression and more than 38%PAE from a +8V supply.

The chip has surface passivation for protection and backside via holes and gold metallization to allow a conductive epoxy die attach process. This device is well suited for communications, Point to Point radio and radar applications.

## Functional Diagram



## Electrical Performance

$T_A=25^{\circ}\text{C}, V_D=+8\text{V}, I_D=2.5\text{A}, Z_0=50\Omega, \text{CW}$

Parameter	Min.	Typ.	Max.	Units
Frequency Range	13.5~14.75			GHz
Small Signal Gain	20	24	—	dB
Small Signal Gain Flatness	—	±1.5	—	dB
Reverse Isolation	—	-55	—	dB
Input Return Loss	—	-10	—	dB
Power-Added Efficiency	—	38	—	%
Output Power for 1 dB Compression (OP <sub>-1dB</sub> )	39.5	40.3	—	dBm
Output Third Order Intercept (OIP <sub>3</sub> ) *	—	44	—	dBm
Drain Voltage (V <sub>D</sub> )	—	8	8.5	V
Gate Current	—	15	70	mA
Supply Current (I <sub>D</sub> )	—	3.5	4.5	A
Thermal Resistance	—	4.2	—	°C/W

\* Measurement taken at Pin / Tone = -7 dBm, f<sub>c</sub> = 14GHz, Δf = 10MHz

## Absolute Maximum Ratings

Maximum Input Power	+22dBm	Operating Temperature	-40°C~+70°C
Channel Temperature	+150°C	Storage Temperature	-65°C~+150°C
Maximum V <sub>D</sub>	+9V	Maximum V <sub>G</sub>	-1.2V

## SuperApex, LLC

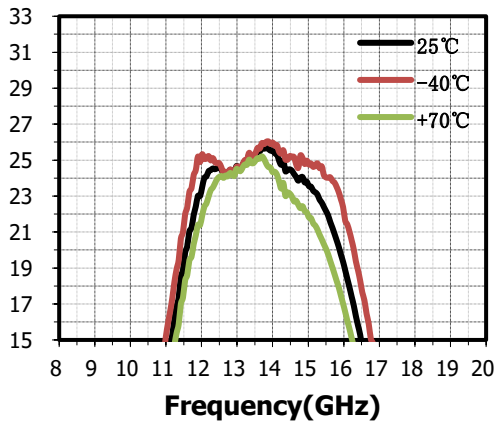
1580 S. Milwaukee Ave. Suite 405, Libertyville, IL 60048, USA  
Tel: 1-847-505-8319, 1-847-573-9866  
E-mail: sales@superapexco.com  
Website: www.superapexco.com

## Typical Small Signal Performance Curve

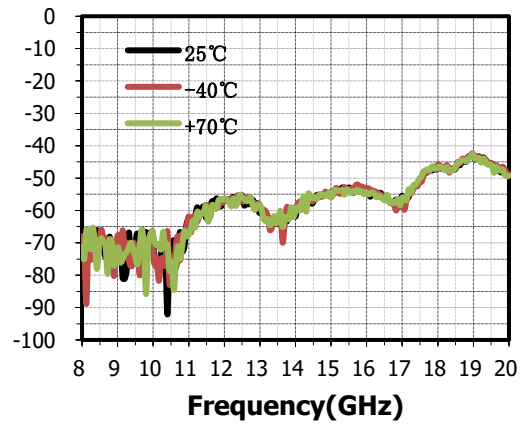
The results captured in the test-jig environment within connector plan

$V_D = +8v$   $I_D = 2.5A$

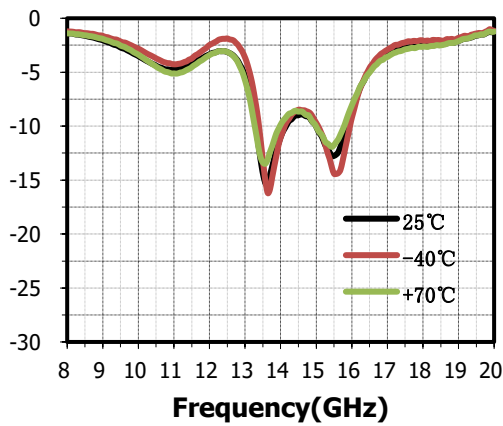
### Small Signal Gain(dB) vs.Temperature



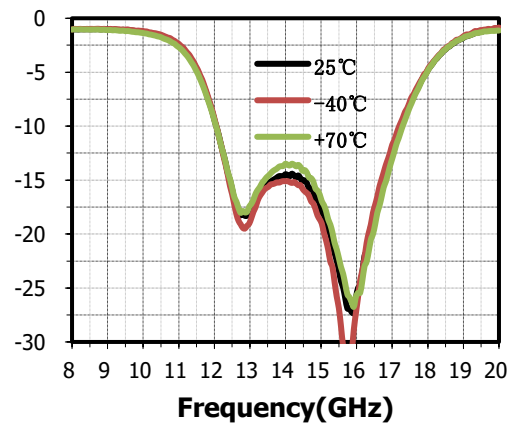
### Reverse Isolation(dB) vs.Temperature



### Input Return Loss(dB) vs.Temperature



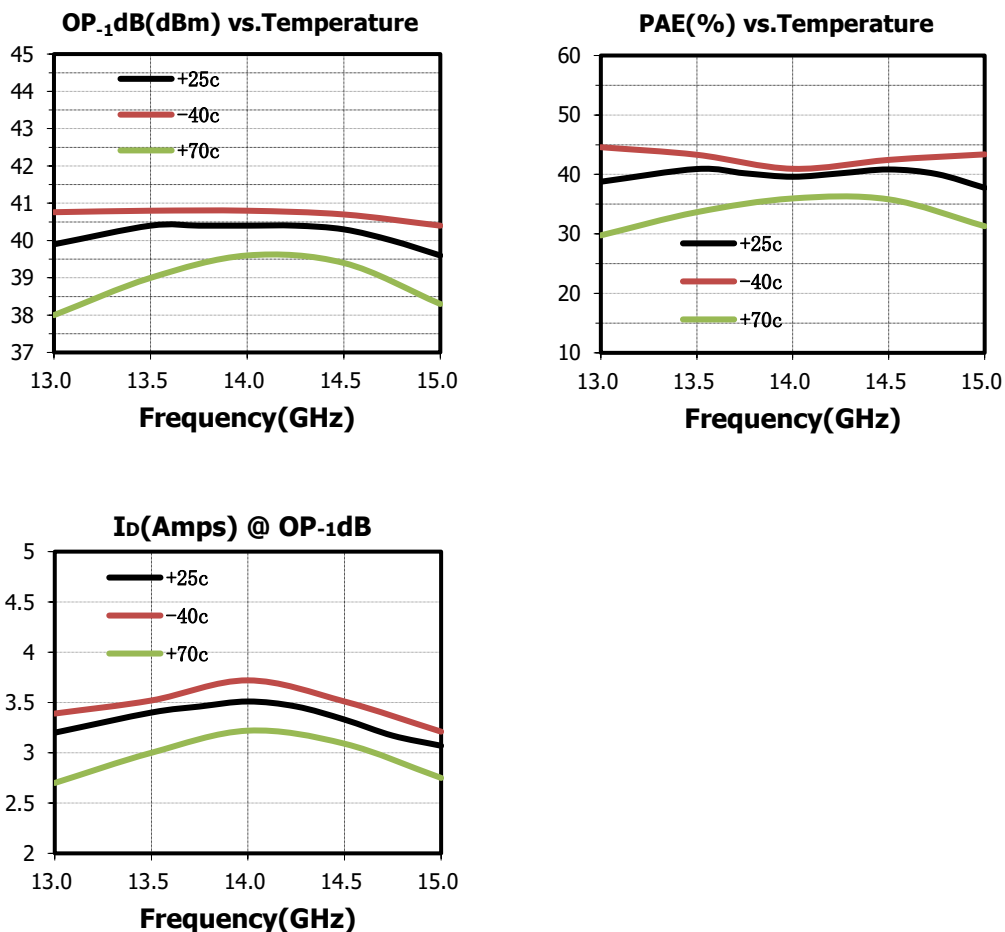
### Output Return Loss(dB) vs.Temperature



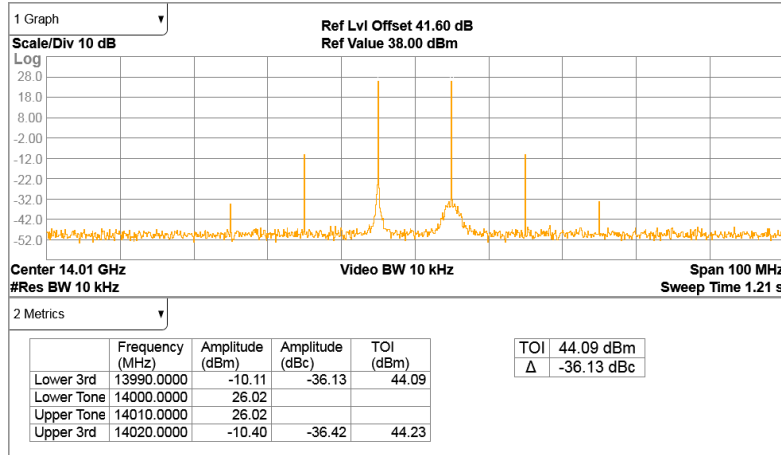
## Power and PAE Performance Curve

The results captured in the test-jig environment within connector plan, then de-embedded the housing an come back in the die plan

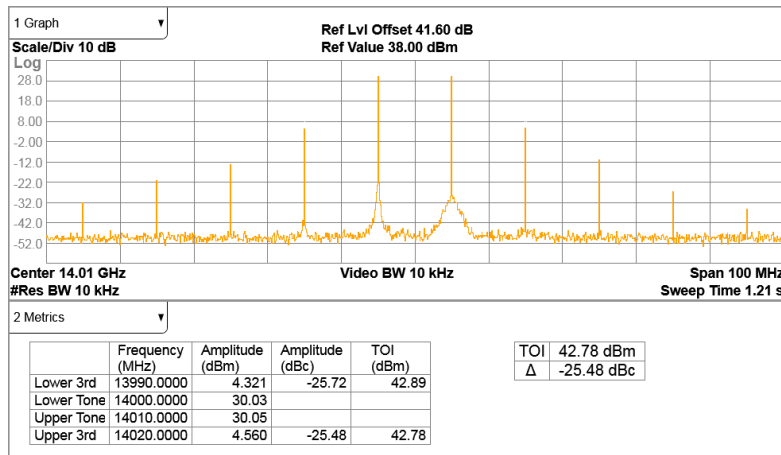
**$V_D = +8v$   $I_D = 2.5A$  CW**



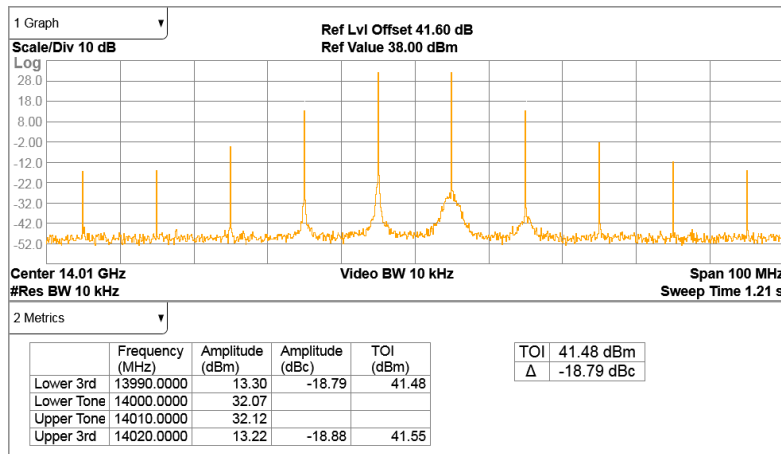
## Output IP<sub>3</sub> Performance Curve Fc=14GHz Pout/Tone=26dBm



## Fc=14GHz Pout/Tone=30dBm



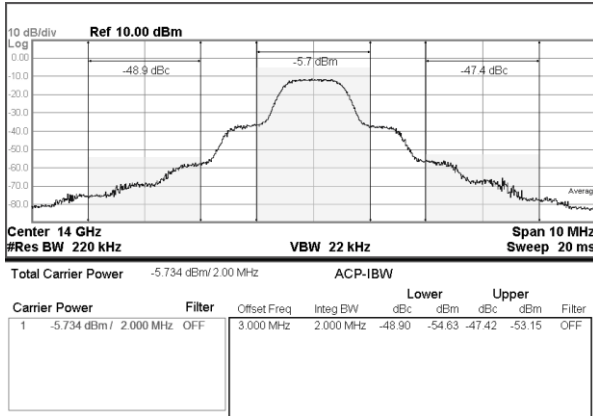
## Fc=14GHz Pout/Tone=32dBm



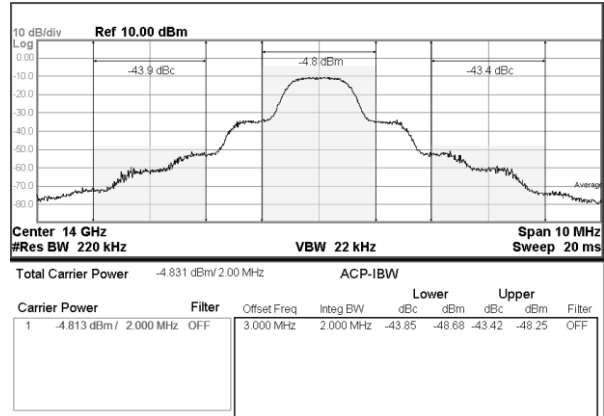
## ACPR Characteristics

QPSK Symbol Rate=1M Filter  $\alpha = 0.3$

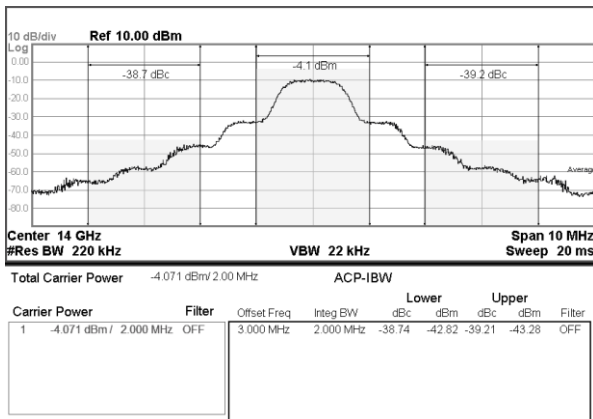
Fc=14GHz Pout=37dBm



Fc=14GHz Pout=38dBm

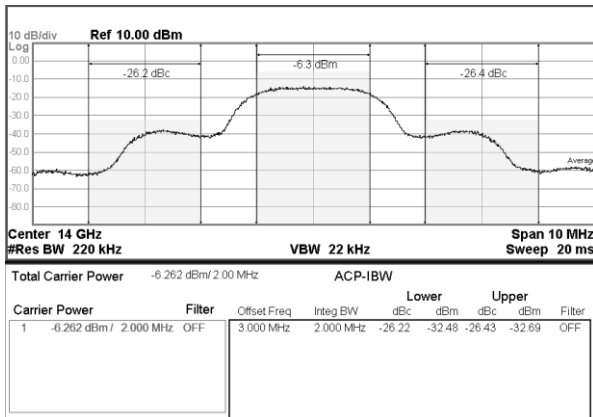


Fc=14GHz Pout=39dBm

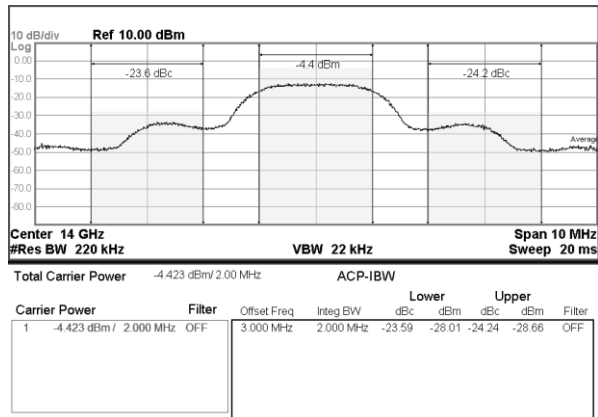


8PSK Symbol Rate=2M Filter  $\alpha = 0.5$

Fc=14GHz Pout=37dBm



Fc=14GHz Pout=39dBm

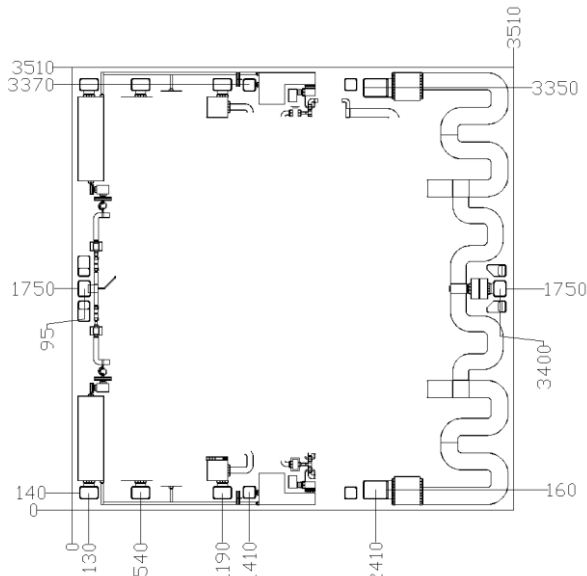


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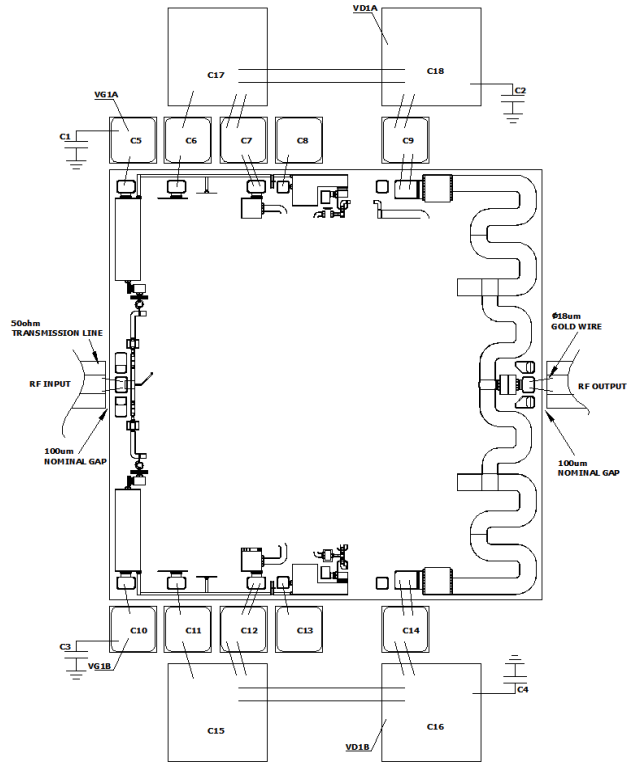
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**Die Outline**  
(All dimensions in um)



**Assembly Diagram**



**Bonding pad size:** 120x90um VG, VD1 ~ VD2, RF IN, RFOUT pads  
90x90um VG3 pads  
200x140um VD3 pads

## Components List

Reference Des.	Value	Part Number	Manuf.	Size
C1~C4	2.2uF	GRM155R61A225KE15D	Murata	0402
C5~C14	300pF	—	ANY	SLC
C15~C18	1000pF	—	ANY	SLC

## Notes

1. SAC3115 is biased with a positive drain supply and negative gate supply. The recommended gate voltage is set to -0.7~-0.8 V.
2. RF connections should be made as short as possible to reduce the inductive effect of the bond wire. Use of a 0.8 mil thermosonic wedge bonding is highly recommended as the loop height will be minimized. The RF input and output require a double bond wire as shown.
3. The backside of SAC3115 is RF ground. Eutectic mounting is preferred, If using conductive epoxy, recommended epoxies are Die Mat DM6030HK or DM6030HK-Pt cured per the manufacturer's cure schedule. Epoxy should be applied in accordance with the manufacturer's specifications and should avoid contact with the top surface of the die. An epoxy fillet should be visible around the total die periphery.
4. Bypass caps C1~C4 should be placed no farther than 1.5mm from the amplifier.

## SuperApex, LLC

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