

# SAC3066Q3



GaAs MMIC Low Noise Amplifier  
7GHz~11GHz

Rev 3.4

## Features

- Frequency: 7GHz~11GHz
- Gain: 21.5dB
- Noise Figure: 1.2dB
- OutputP<sub>1</sub>dB: 16dBm
- Package Size: QFN3x3mm

## Typical Applications

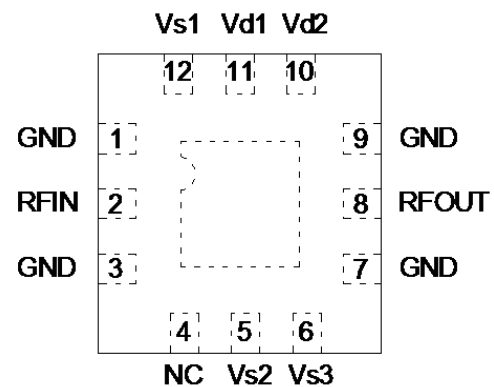
- Radar and ECM
- RF/ Microwave radio
- Test and Measurement

## General Description

SAC3066Q3 is a GaAs MMIC Low Noise Amplifier in leadless 3x3mm surface mount package, which operates between 7GHz~11GHz. The amplifier can provide 21.5dB gain, 16dBm OutputP<sub>1</sub>dB, 1.2dB noise figure from a +5V supply voltage.

The amplifier I/O's are internally matched to 50 Ohms.

## Functional Diagram



## Electrical Performance ( T<sub>A</sub>=25°C, V<sub>D</sub>=+5V, I<sub>D</sub>=42mA, Z<sub>0</sub>=50Ω )

Parameter	Min	Typ.	Max	Units
Frequency Range	7~11			GHz
Gain	20	21.5	23	dB
Gain Flatness	—	±1.5	—	dB
Reverse Isolation	-30	-34	—	dB
Input/Output Return Loss	—	-12	-8	dB
Noise Figure	—	1.2	1.4	dB
Output Power for 1 dB Compression (OP <sub>1</sub> dB)	13	16	—	dBm
OIP <sub>3</sub>	—	28	—	dBm

## Absolute Maximum Ratings

Maximum Input Power	+20dBm	Operating Temperature	-55°C ~ +85°C
Channel Temperature	150°C	Storage Temperature	-65°C ~ +150°C
Maximum V <sub>D</sub>	+6V		

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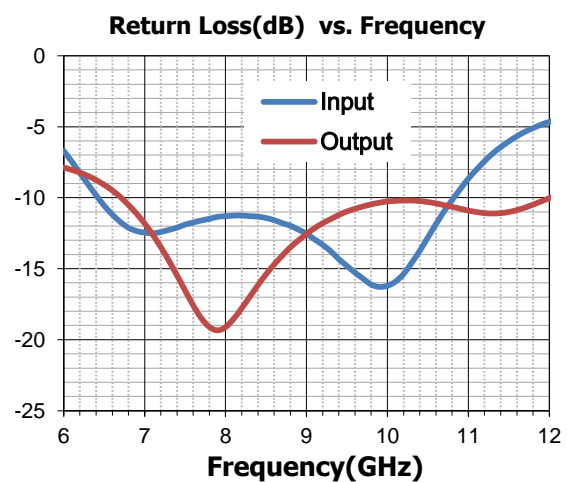
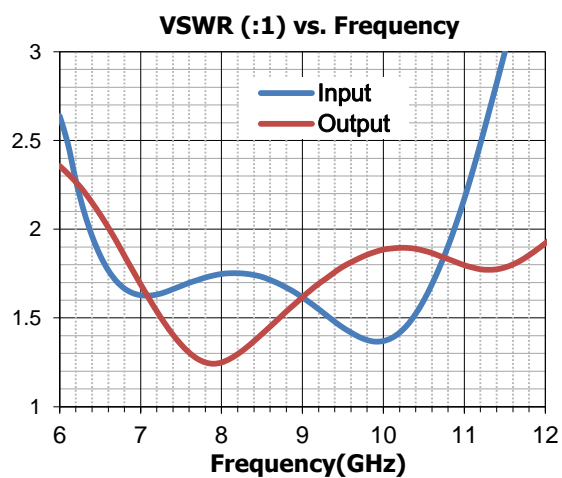
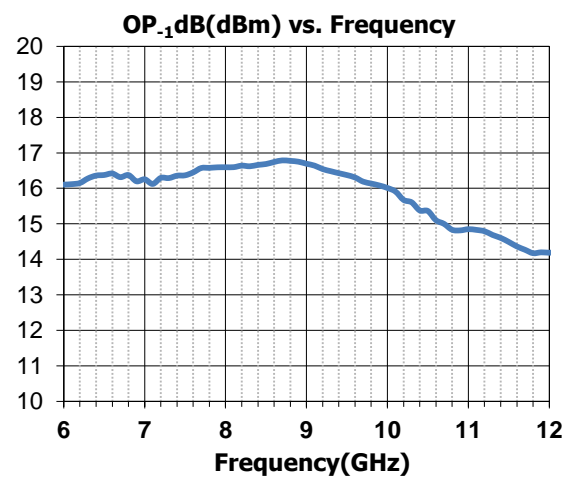
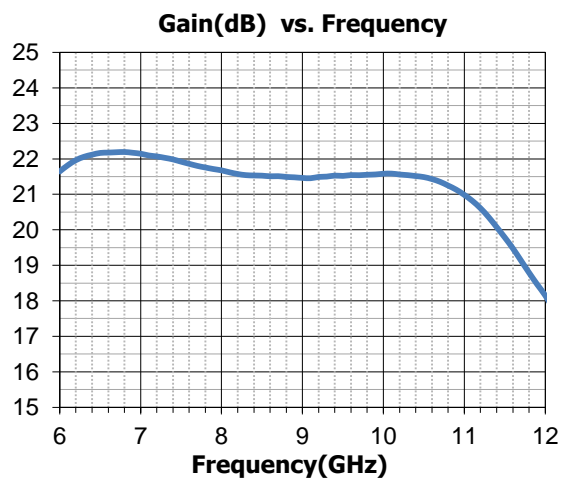
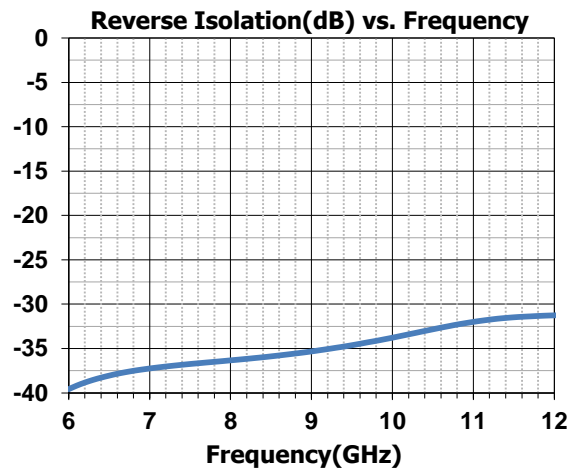
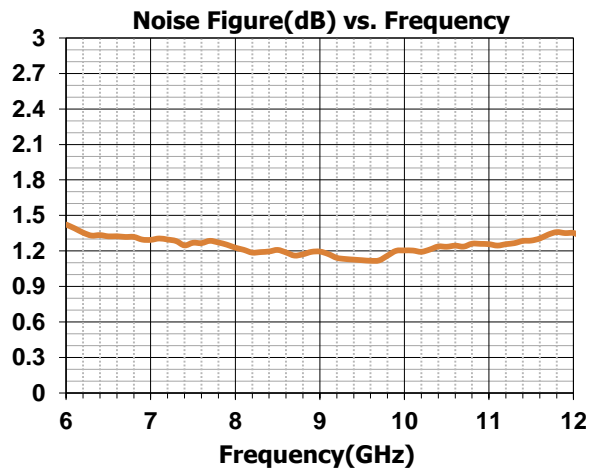
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## Typical Performance Curve

LNA bias at  $V_{d1}=5V, I_{d1}=15mA$ ,  $V_{d2}=5V, I_{d2}=30mA$

$V_{s1}, V_{s2}, V_{s3}$  no connect to GND



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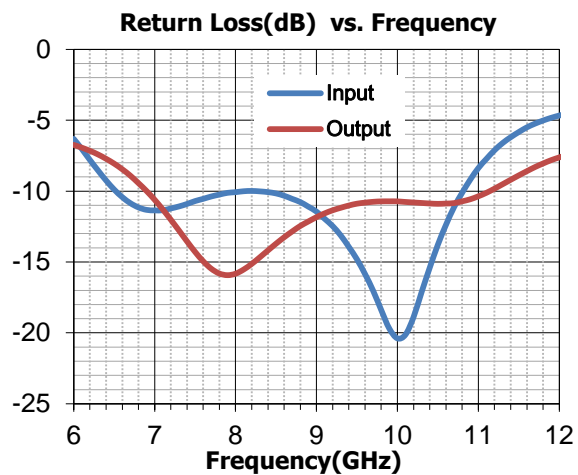
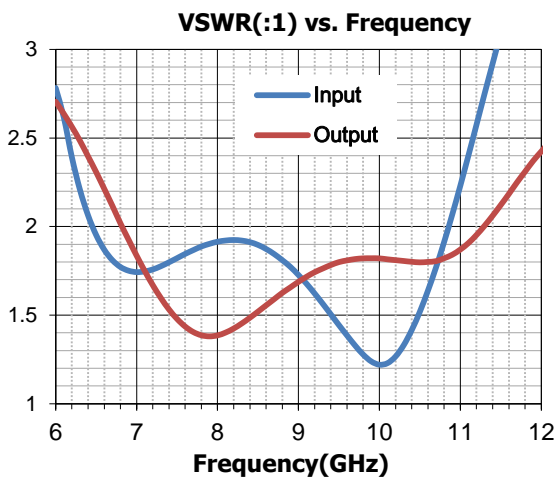
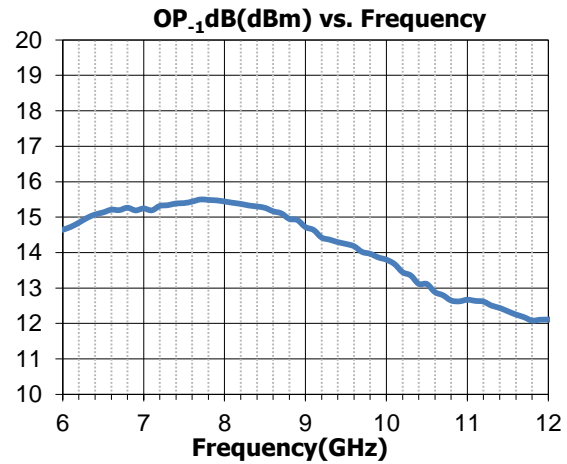
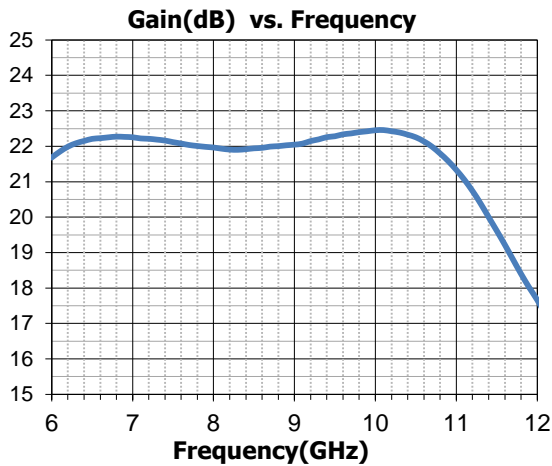
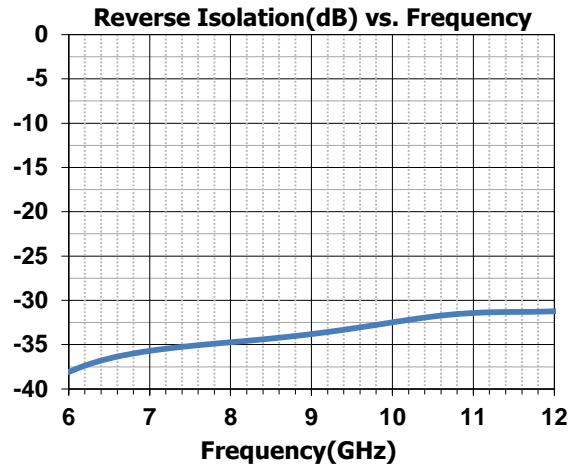
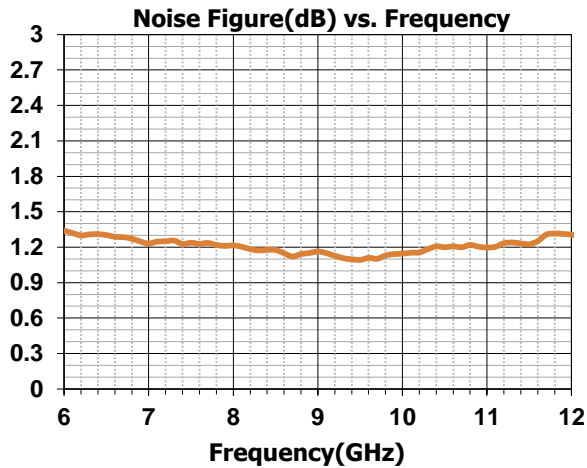
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## Typical Performance Curve

LNA bias at  $Vd1=3V, Id1=27mA, Vd2=4V, Id2=28mA$   
(Vs1 connect to GND). the lowest noise figure Application



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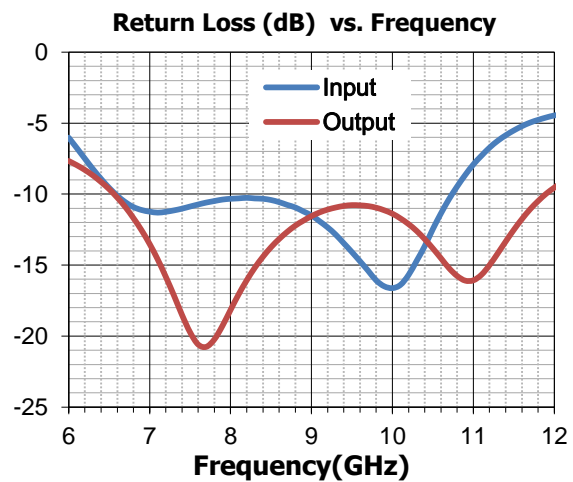
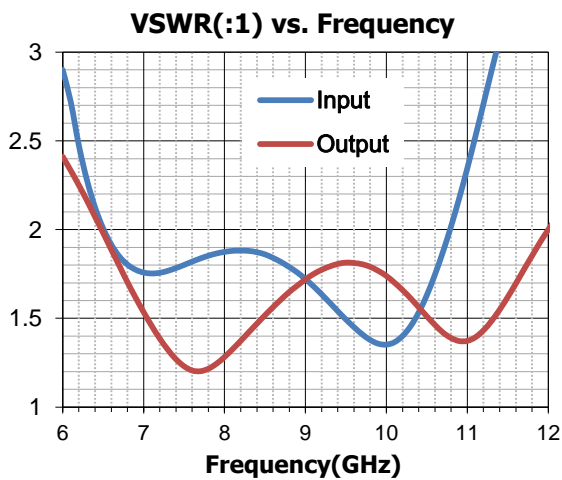
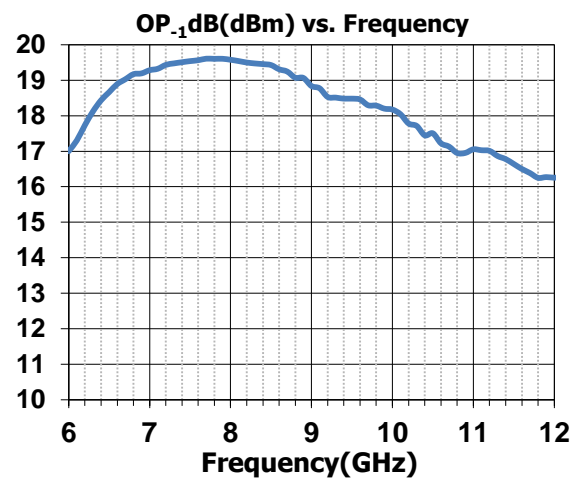
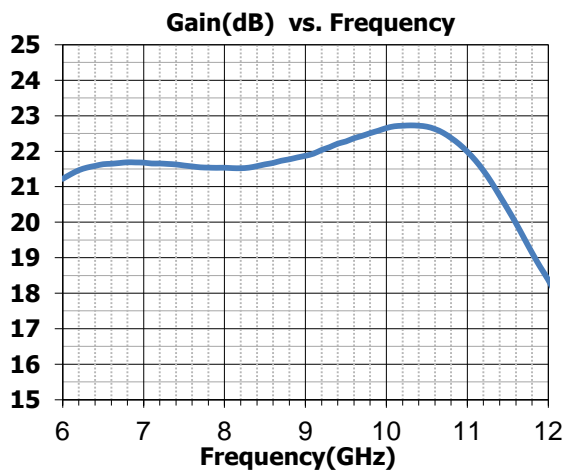
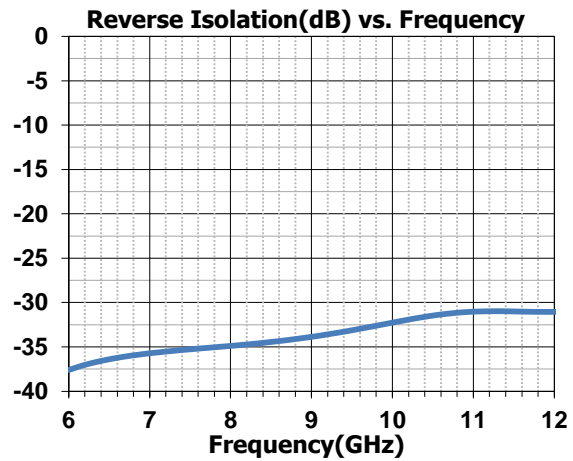
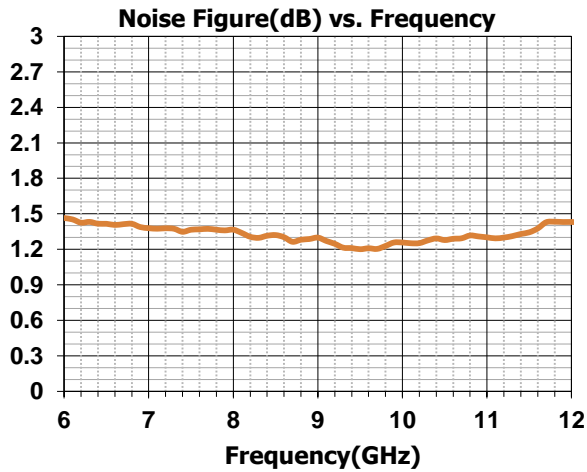


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## Typical Performance Curve

LNA bias at  $V_{d1}=3V, I_{d1}=15mA, V_{d2}=6V, I_{d2}=56mA$   
( $V_{s3}$  connect to GND). the highest  $P_{-1dB}$  Application



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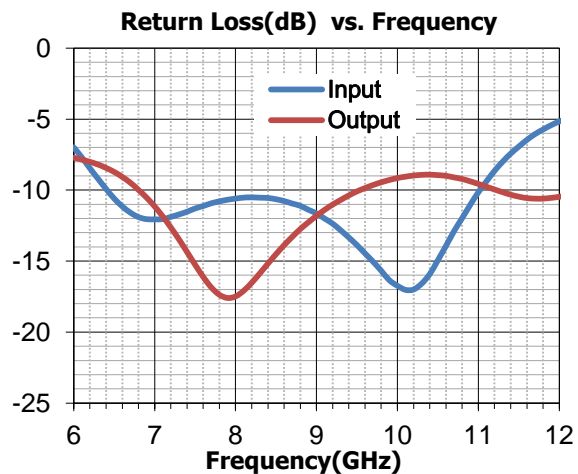
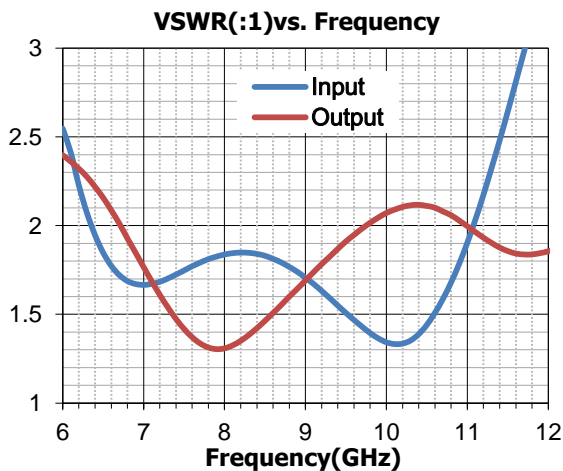
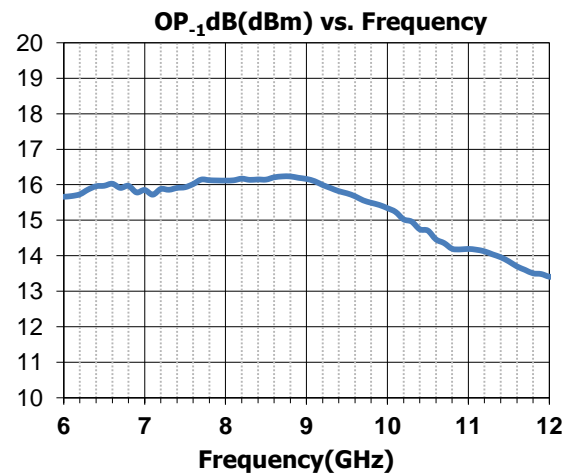
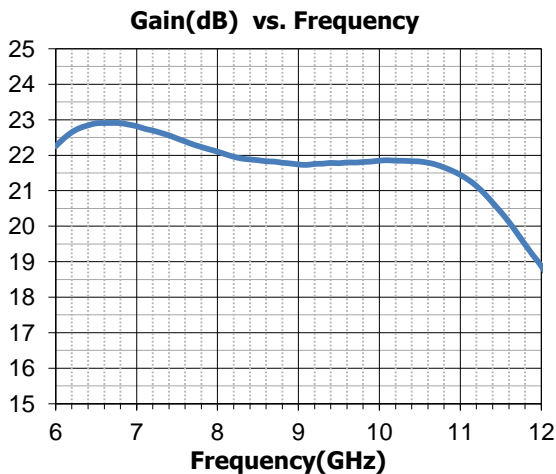
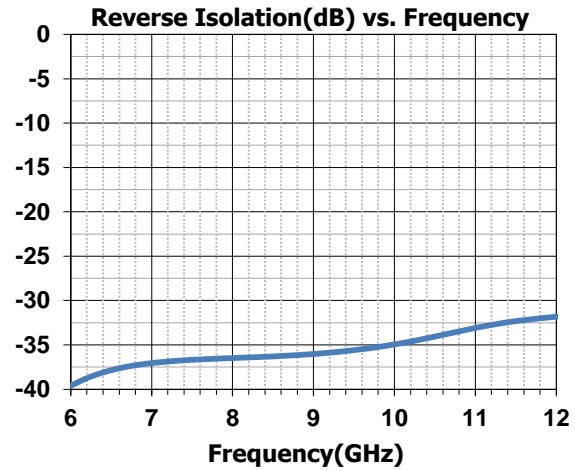
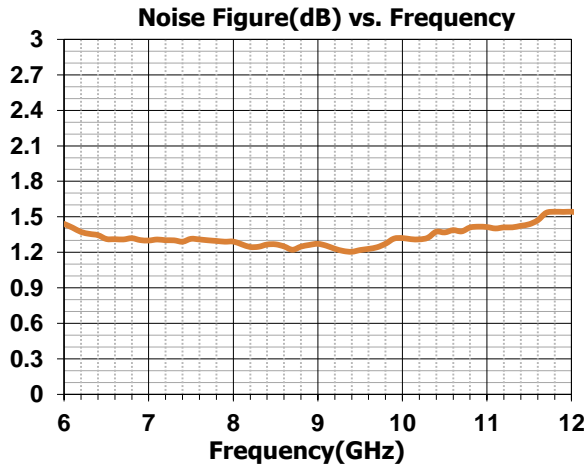


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## Typical Performance Curve

LNA bias at  $V_{d1}=5V, I_{d1}=28mA, V_{d2}=5V, I_{d2}=30mA$   
(Vs1 connect to GND).



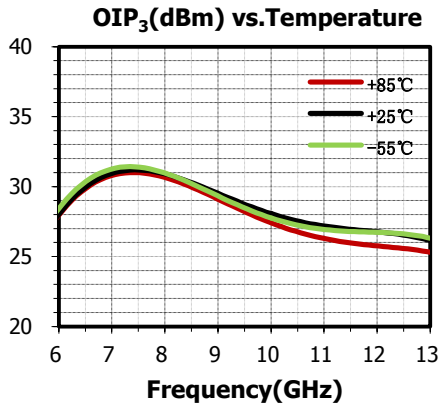
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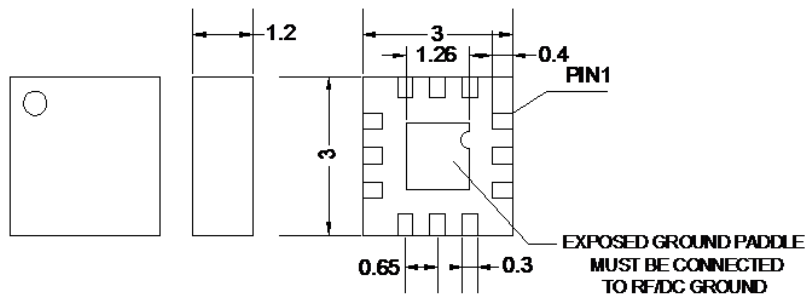
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## Outline Drawing

(All dimensions in mm)



## Pin Function

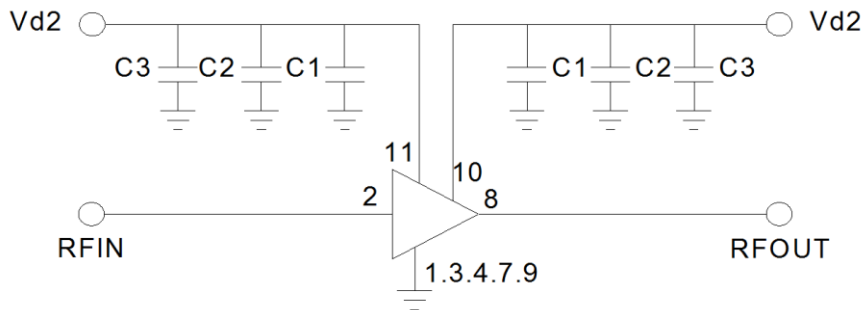
Pin No.	Func.	Pin No.	Func.
1	Connect to GND	7	Connect to GND
2	RF IN	8	RF OUT
3	Connect to GND	9	Connect to GND
4	NC	10	Vd2 Supply
5	Vs2	11	Vd1 Supply
6	Vs3	12	Vs1

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## Assembly Diagram



## Component list

Reference Des.	Value	Part Number	Manuf.	Size
C1	100pF	GCM1555C1H101JA16D	MURATA	0402
C2	1000pF	GRM1555C1H102JA01D	MURATA	0402
C3	0.1μF	GCM155R71C104JA55D	MURATA	0402

### Attention:

1. The moisture resistant grade of products is 2A, the storage environment  $\leq 30^{\circ} \text{C}/60\% \text{RH}$ , The surrounding workshop Life is 4 weeks.
2. After un-packing, It is necessary to bake the parts for 6 hours in  $125 \pm 5$  degree environment before soldering.