

Features

- Frequency: 0.05~2GHz
- Small Signal Gain: 13dB
- Output Power: 38dBm
- PAE: 33%
- IM3: -25dBc@Pout/Tone 32dBm
- Die Size: 2.1mm×2.67mm×0.1mm
- Package: Bare die
- Supply Voltage: +24V/-Vg

Typical Applications

- Point-to-Point Radios
- SATCOM

General Description

SAC5001 is a high linearity power amplifier operates from 0.05–2GHz and provides 6W of saturated output power with 10dB of large signal gain and greater than 30% power-added efficiency, no external matching is required to achieve full-band operation

Electrical Performance

$T_{BASE}=23^{\circ}C$, $V_D=+24V$, $I_{DQ}=60mA$, $Z_0=50\Omega$, CW

Parameter	Min.	Typ.	Max.	Units
Frequency Range	0.05	—	2	GHz
Small Signal Gain	10	13	—	dB
Power Gain	—	10	—	dB
Reverse Isolation	—	-30	—	dB
RF Input Port Return Loss	—	1.7	—	:1
Output Power	—	38	—	dBm
Drain Voltage (V_D)	—	24	—	V
Gate Current	—	1	5	mA
Supply Current (I_D)*	—	—	1.2	A

*Adjust Vg between -2.8V to -1.5V to achieve $I_{DQ}=60mA$, and typical Vg voltage is -2.4V

Absolute Maximum Ratings

Maximum Input Power	+31dBm	Operating Temperature (T_{BASE})	-55°C~+85°C
Channel Temperature	230°C	Storage Temperature	-55°C~+180°C
Maximum V_D	+32V	V_G Range	-5V~-1.5V
Mounting Temperature	310°C,50s		

SAC5001



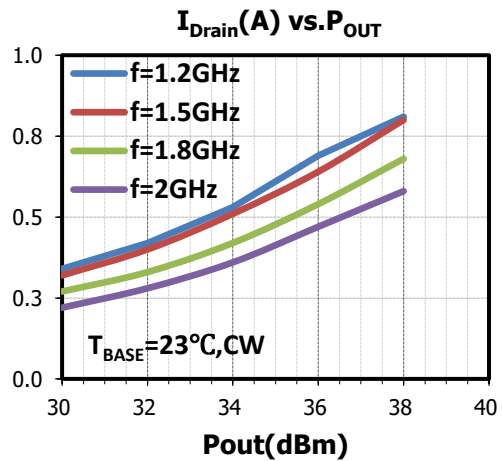
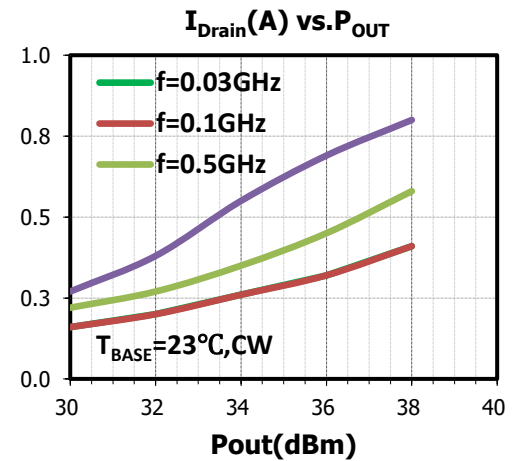
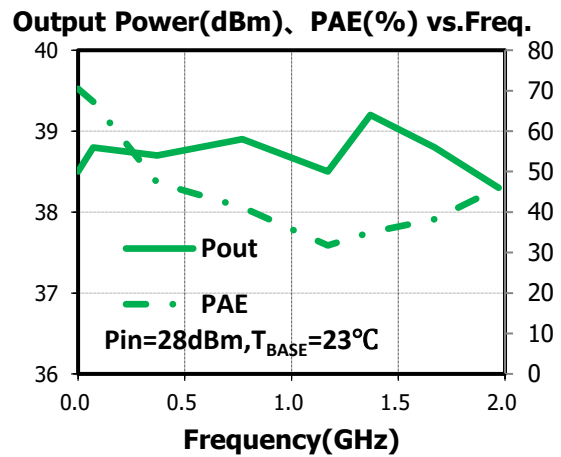
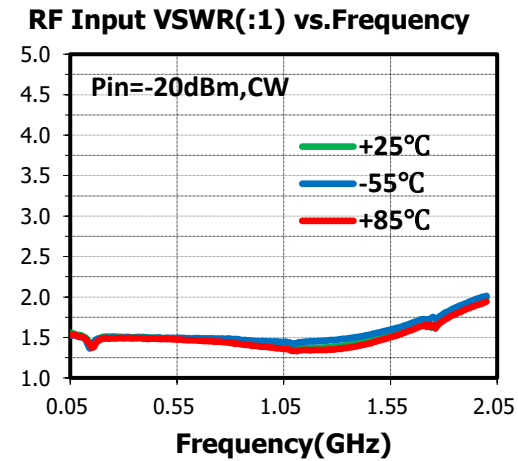
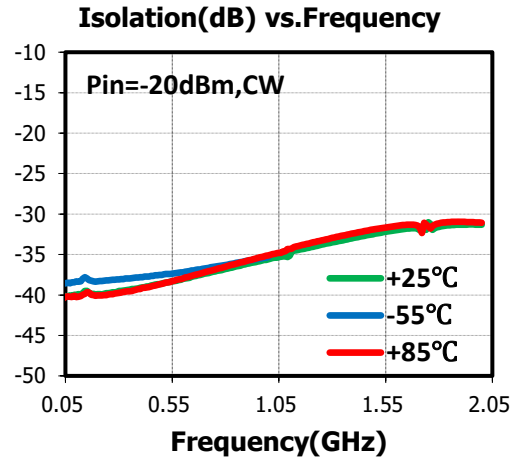
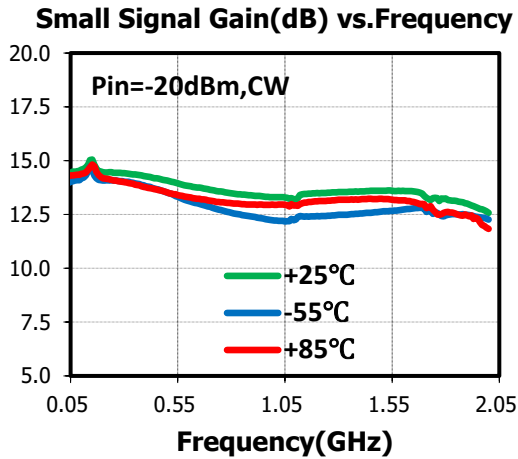
GaN MMIC Power Amplifier
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Rev 1.0

Typical Performance Curve

The following data was tested using the SAC5001 evaluation board, $V_D = +24V$, $I_{DQ} = 60mA$, $T_{BASE} = +23^\circ C$, $C_{Block} = 1000pF$,

$L_{Bias} = 11$ Turns



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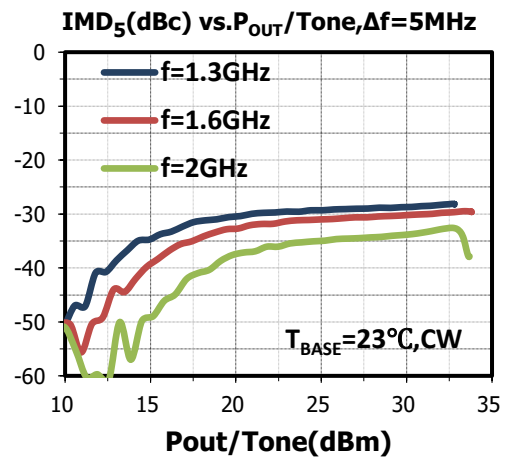
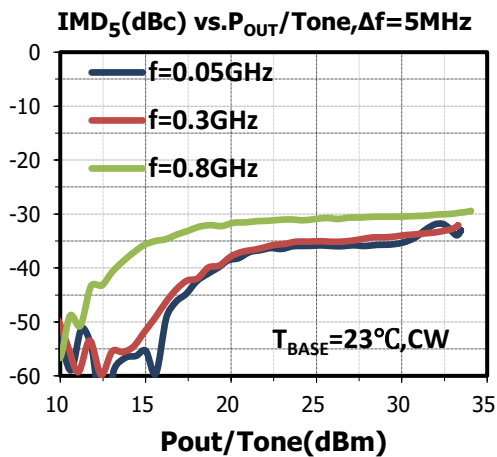
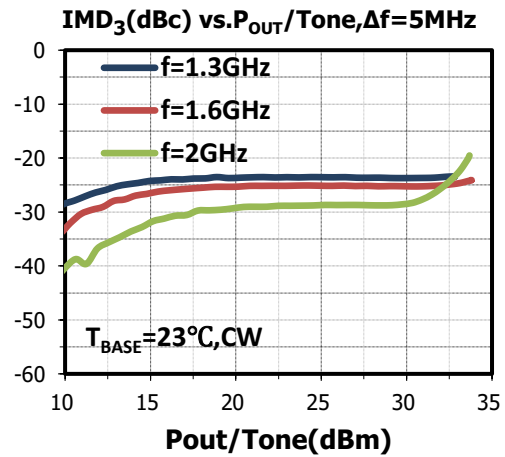
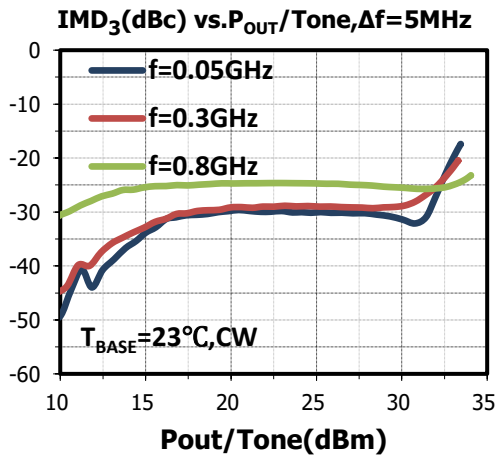
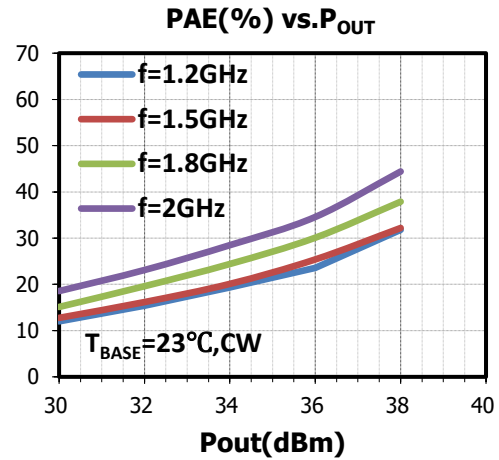
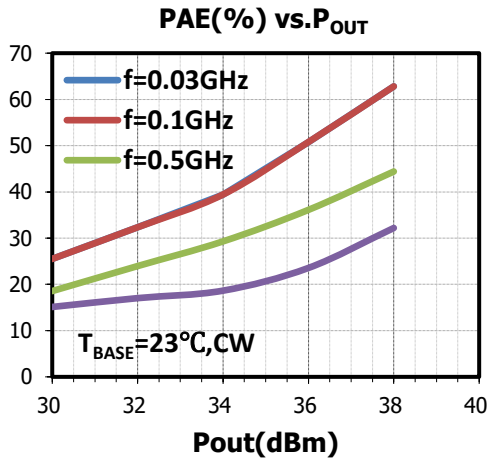
1580 S. Milwaukee Ave. Suite 405, Libertyville, IL 60048, USA
Tel: 1-847-505-8319, 1-847-573-9866
E-mail: sales@superapexco.com
Website: www.superapexco.com

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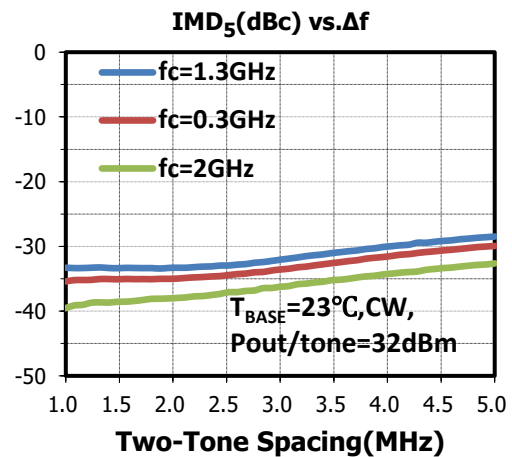
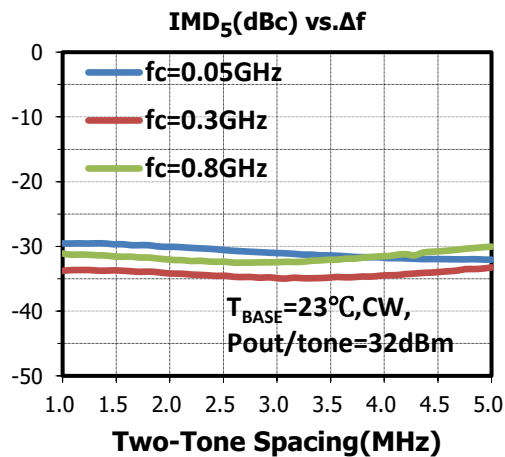
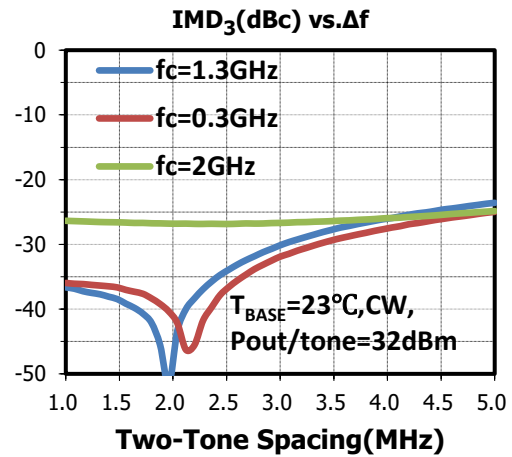
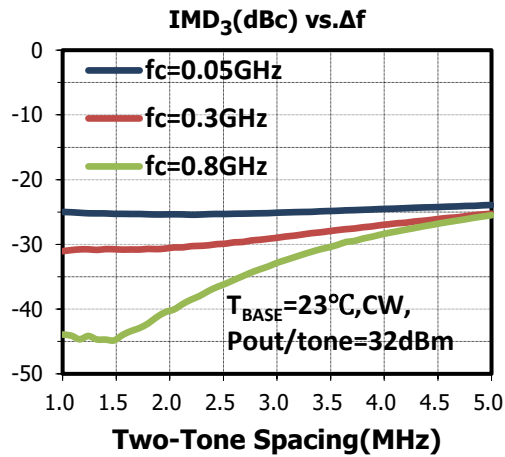
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Thermal Resistance

Parameter	Conditions	Value	Unit
θ_{JC1}	$V_{\text{D}}=+24\text{V}, T_{\text{BASE}}=+70^\circ\text{C}, P_{\text{in}}=+28\text{dBm}, \text{CW}, f=1\text{GHz}$	9.6	$^\circ\text{C}/\text{W}$

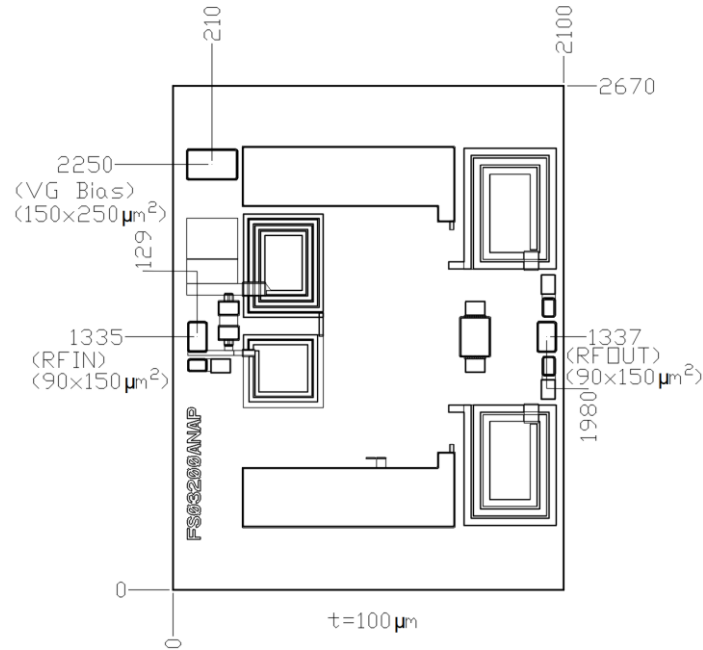
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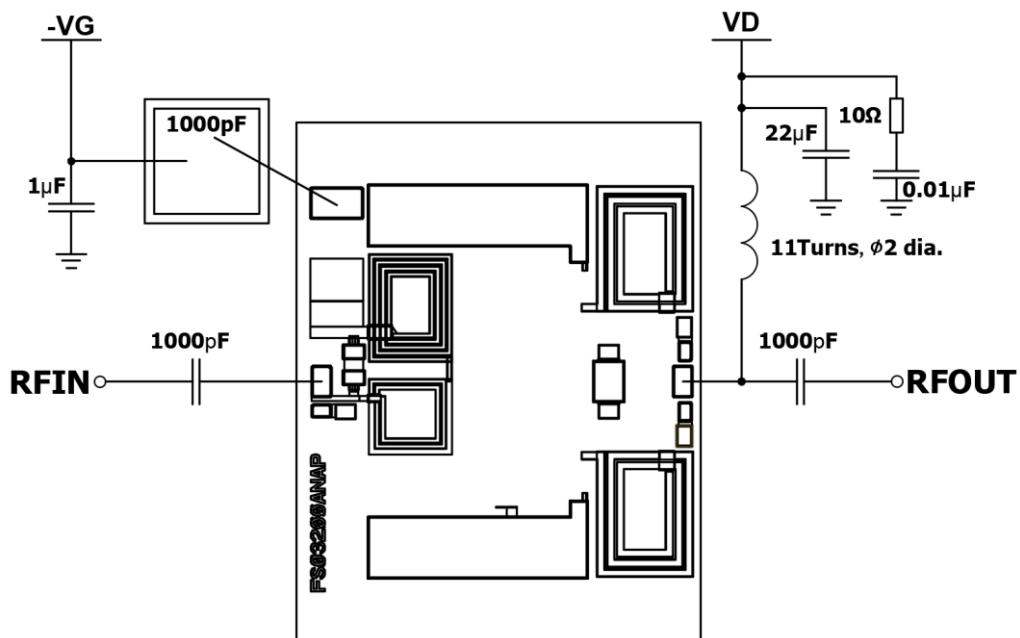
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Bare Die Outline

(μm)



Assembly Diagram



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Notes

1. SAC5001 requires VDx and VGx bias.

Turn-on: Apply VGx, Apply VDx, Apply RFIN signal.

Turn-off: Remove RFIN signal, Decrease VG to -5V (pinch-off), Decrease VD to 0 V

2. Microelectronic devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly, and test.

Revision History

Revision	Date	Comment
1.0	May 11, 2024	First Release

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